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If you update the map on the device using Mio Transfer you are not able to access the map. Step 1. Download a GPX to your PC; Step 2:. Then it will be checked whether new routes are available for transfer. Mio Transfer C520 DownloadSemiconductor devices such as a field effect transistor (FET) and a metal-oxide-semiconductor field-effect transistor (MOSFET) formed on a silicon (Si) substrate have been applied widely in various electric products. For instance, regarding a power MOSFET, a trench gate type MOSFET (Trench MOSFET) can operate at a high voltage and have a low on-resistance, and thus has been widely applied in power circuits. As an example of the trench gate type MOSFET, there is a double diffused MOS (DMOS) transistor. The double DMOS transistor can improve a breakdown voltage and a breakdown resistance of a pn diode, and thus the double DMOS transistor is applied widely as a

power MOSFET. A DMOS transistor using a p-type base layer (PBL) in a channel region has an advantage that a DMOS transistor can be easily formed in an n-type silicon substrate. However, in the DMOS transistor using the p-type base layer (PBL) in the channel region, since a p-type dopant in the p-type base layer (PBL) diffuses into an n-type drift region, there is a disadvantage that an n-type dopant in the drift region is diffused into the p-type base layer (PBL). Further, in order to prevent a punch through phenomenon and the like, a DMOS transistor using the p-type base layer (PBL) in the channel region is manufactured by a strong implantation doping method so that a diffusion amount of the p-type dopant in the p-type base layer (PBL) is increased. Therefore, there is a disadvantage in that an on-resistance of a DMOS transistor is increased. Thus, in order to overcome the above disadvantages, a DMOS transistor using an n-type epitaxial base layer (NEBL) in the channel region may be used. Since an impurity concentration in the n-type epitaxial base layer (NEBL) is higher than